

FIG. 1
high-Q on-chip
inductor 10

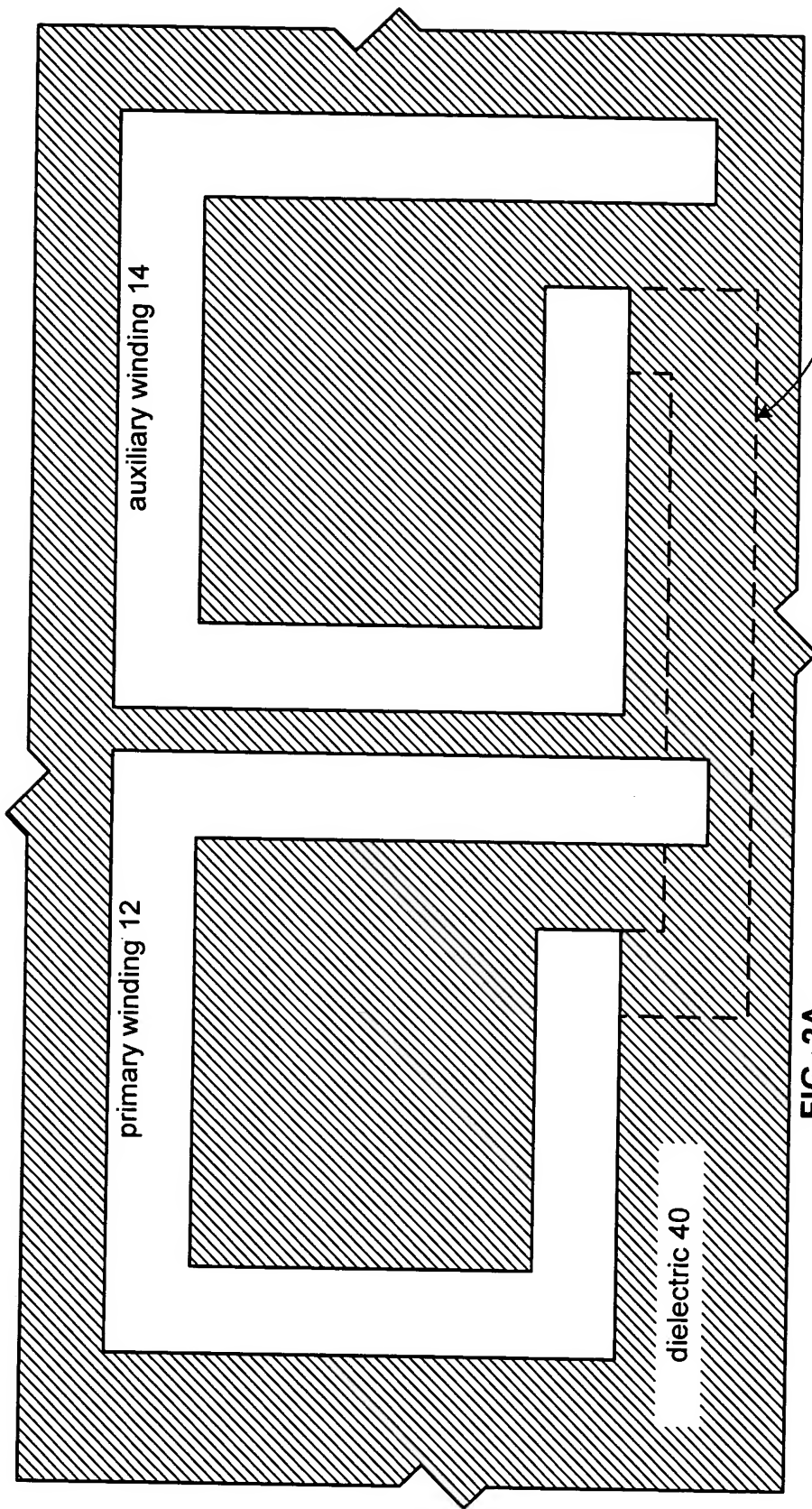


FIG. 2A
TOP VIEW

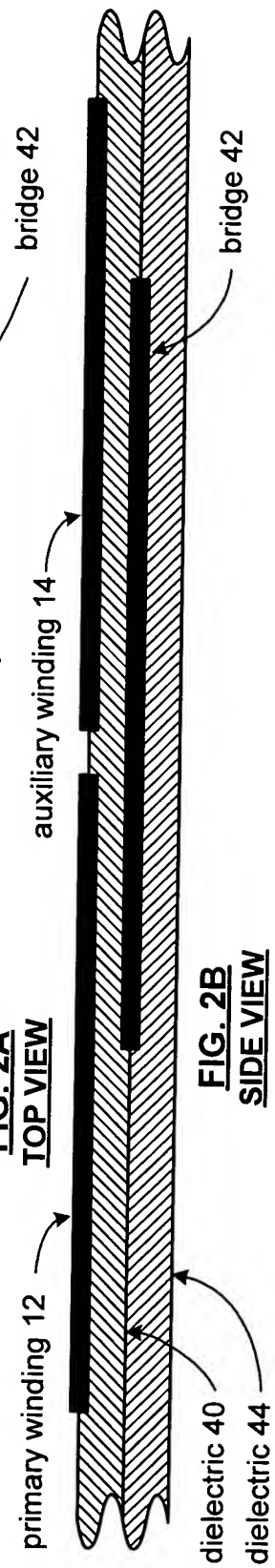
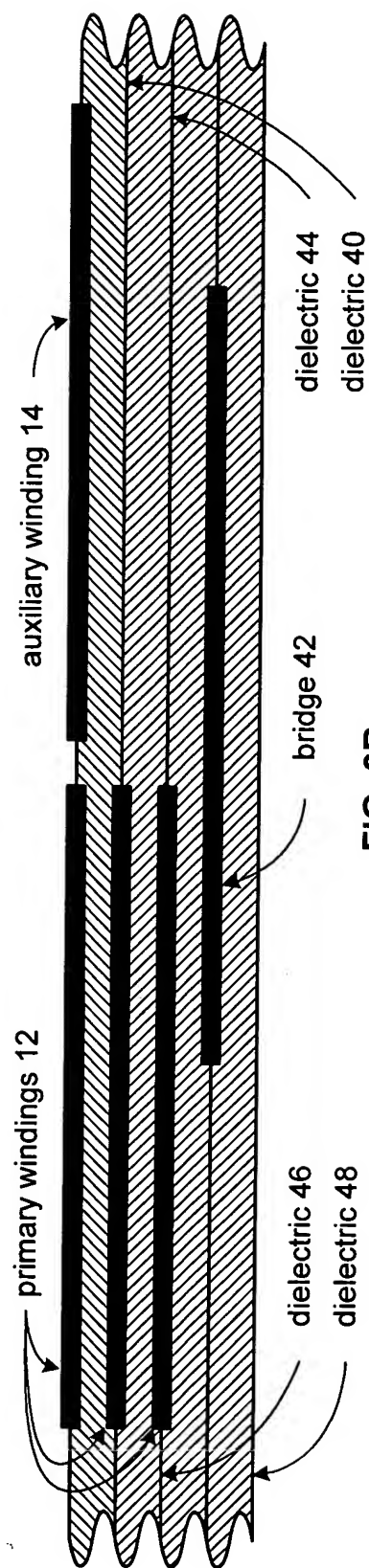
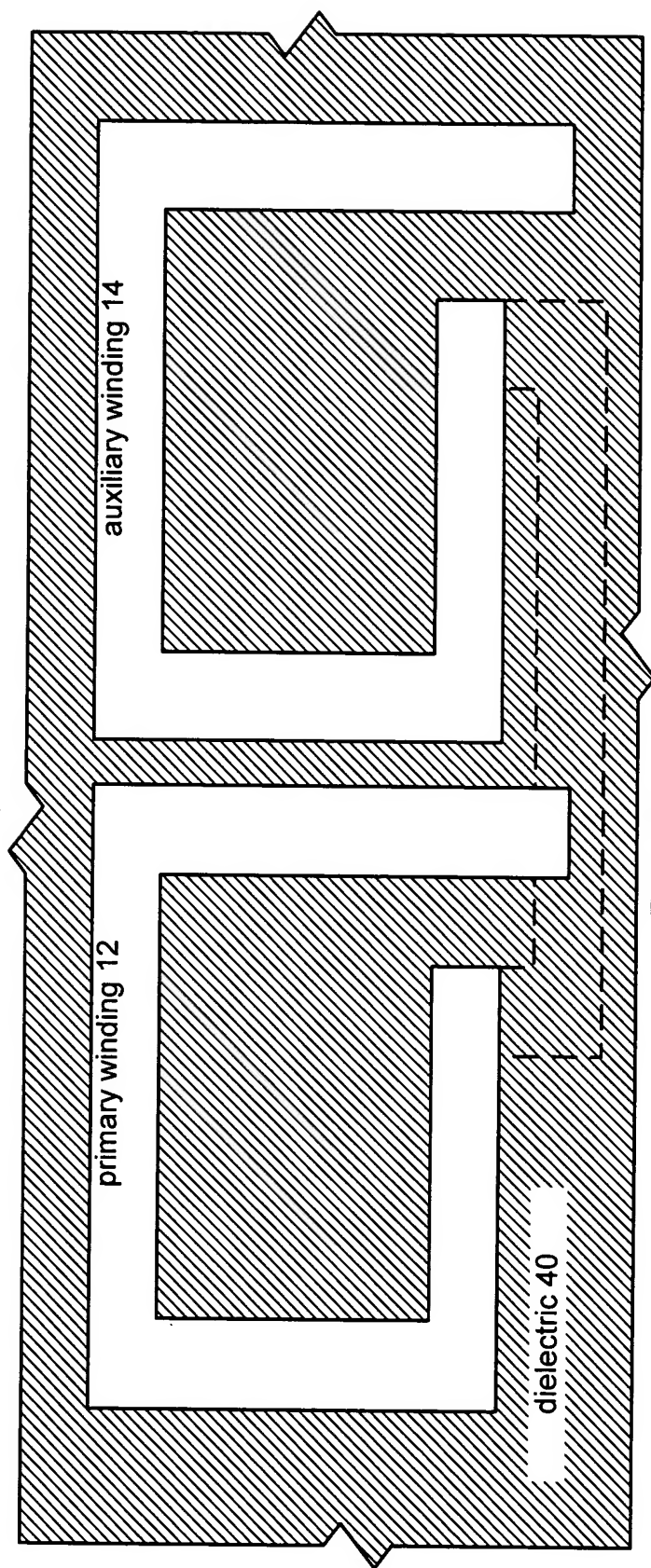


FIG. 2B
SIDE VIEW



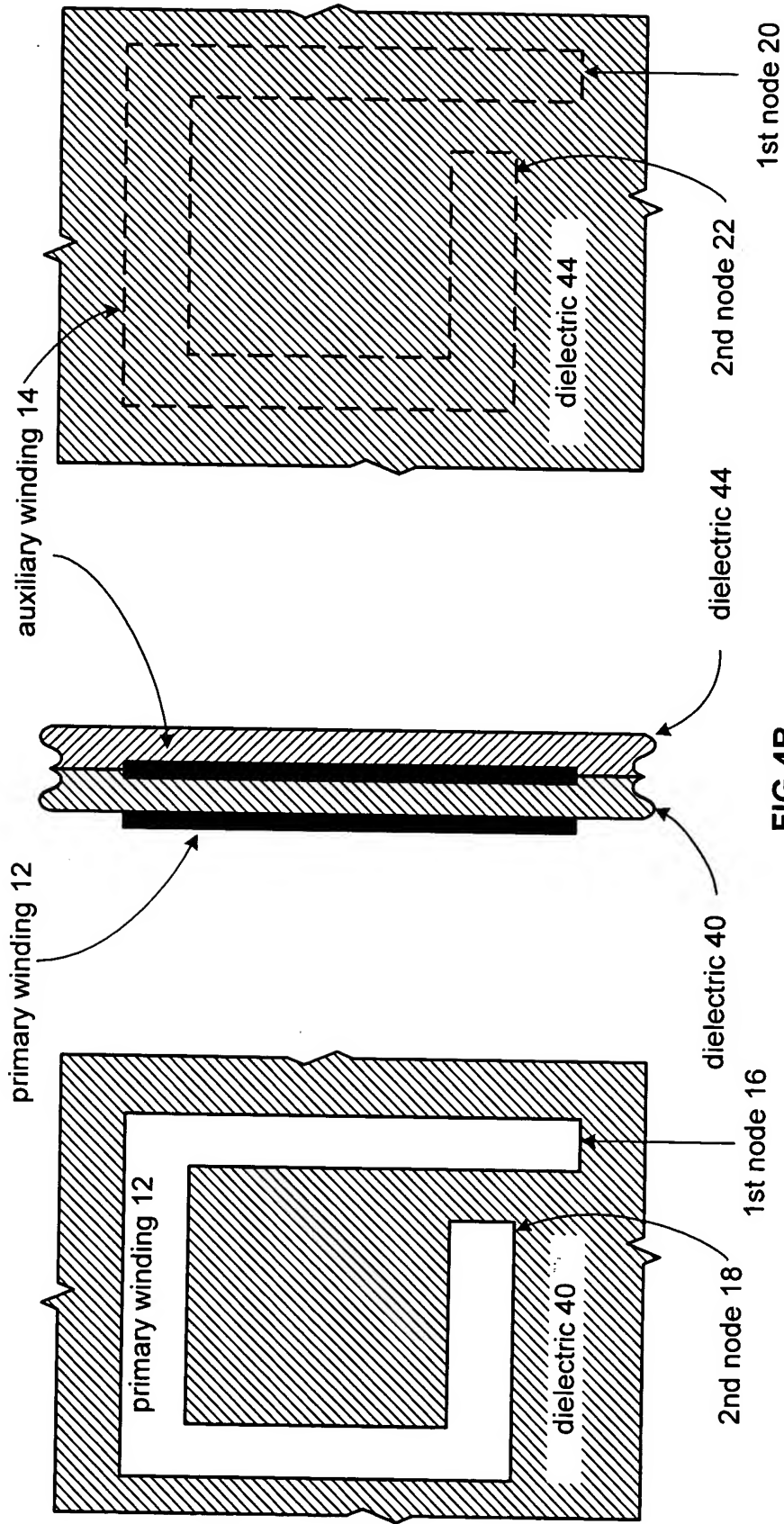


FIG. 4A
TOP VIEW

FIG. 4B
SIDE VIEW

FIG. 4C
BOTTOM
VIEW

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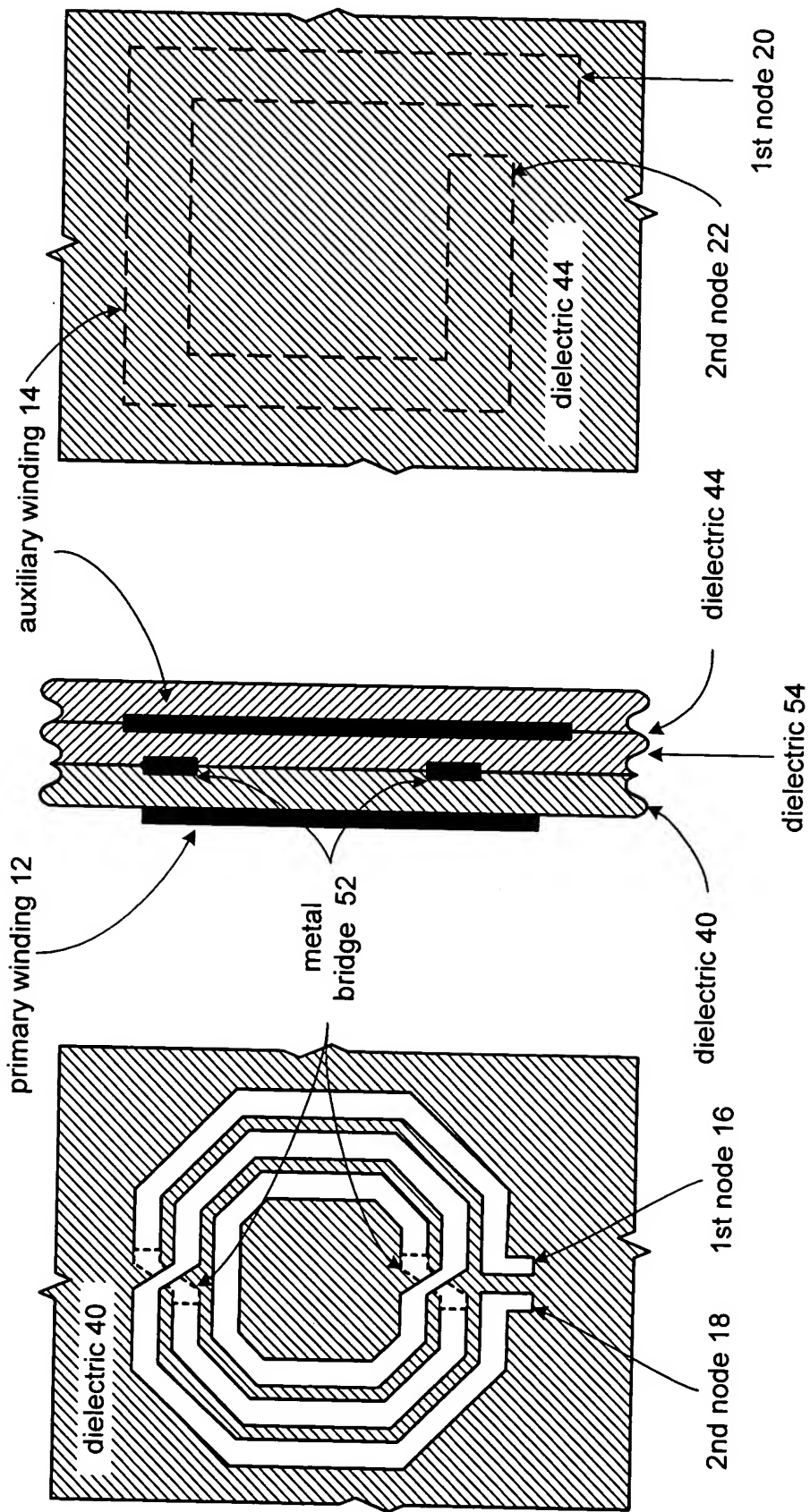
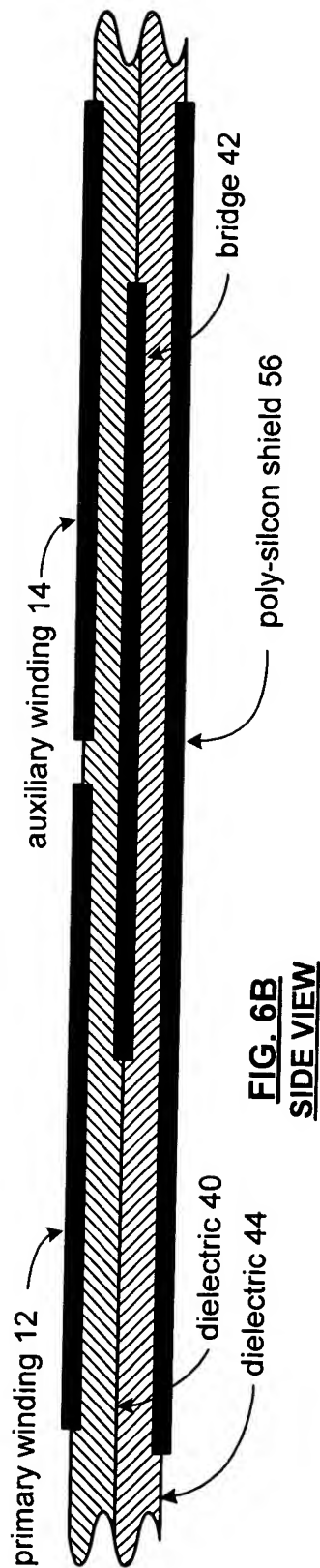
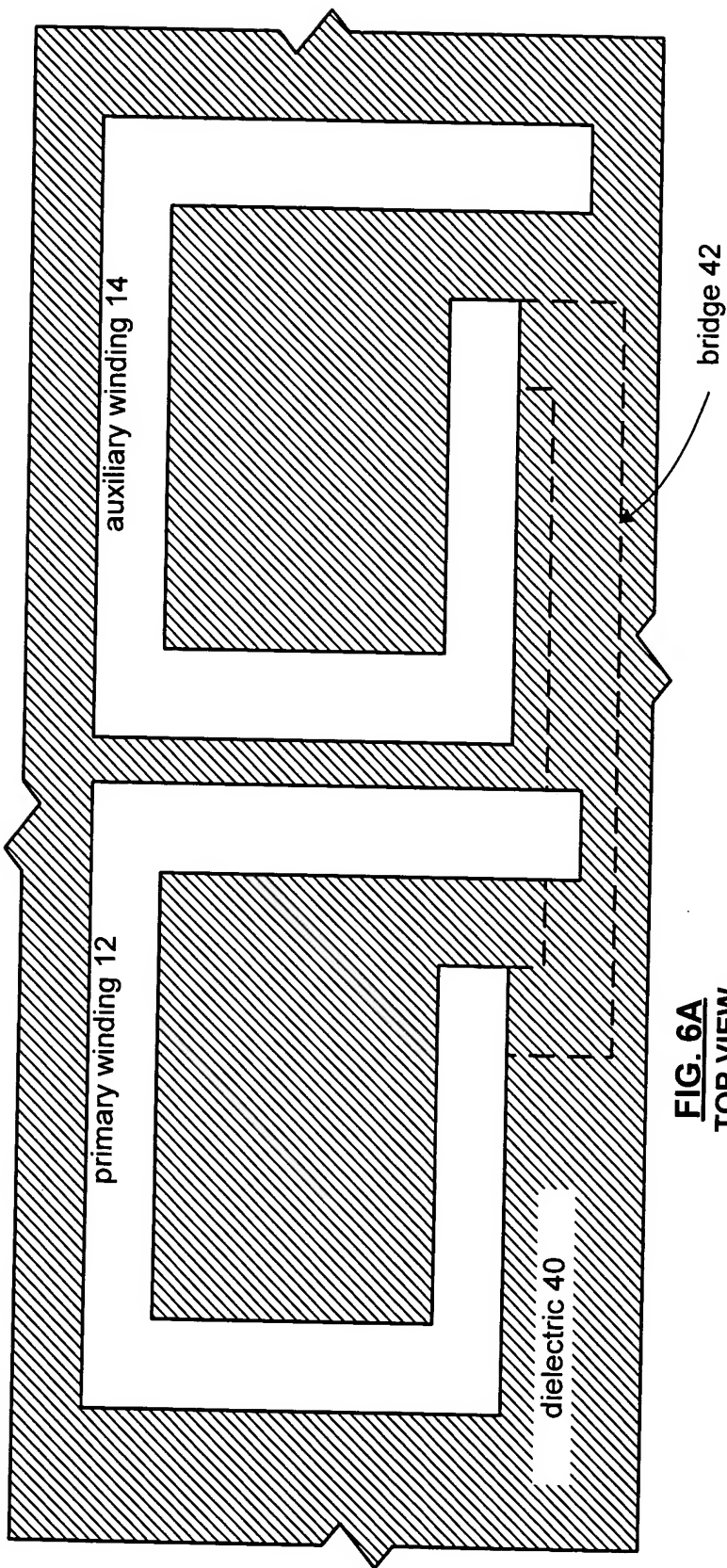


FIG. 5A
TOP VIEW

FIG. 5B
SIDE VIEW

FIG. 5C
BOTTOM VIEW



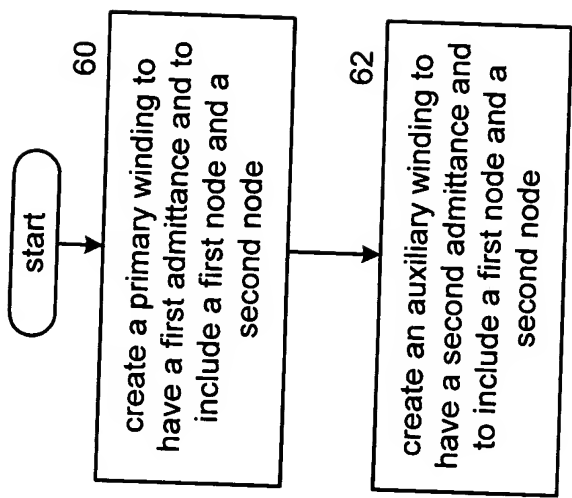


FIG. 7